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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/810,685	03/29/2004	Yair Shemesh	P-6585-US	1249
49444 7590 02/23/2007 PEARL COHEN ZEDEK LATZER, LLP 1500 BROADWAY, 12TH FLOOR			EXAMINER	
			CHOW, CHARLES CHIANG	
NEW YORK, NY 10036			ART UNIT	PAPER NUMBER
		•	. 2618	
SHORTENED STATUTOR	Y PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE	
31 D	DAYS	02/23/2007	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

	Application No.	Applicant(s)			
Office Action Comments	10/810,685	SHEMESH ET AL	SHEMESH ET AL.		
Office Action Summary	Examiner	Art Unit			
	Charles Chow	2618			
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet	with the correspondence as	ddress		
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUN 16(a). In no event, however, may rill apply and will expire SIX (6) M cause the application to become	NICATION. a reply be timely filed ONTHS from the mailing date of this of ABANDONED (35 U.S.C. § 133).			
Status					
1) Responsive to communication(s) filed on 28 Ma	arch 2004.				
	action is non-final.				
3) Since this application is in condition for allower		atters, prosecution as to the	e merits is		
closed in accordance with the practice under E	•	·			
	•				
Disposition of Claims					
4)⊠ Claim(s) <u>1-39</u> is/are pending in the application.			•		
4a) Of the above claim(s) is/are withdraw	n from consideration.	•	-		
5) Claim(s) is/are allowed.					
6)☐ Claim(s) is/are rejected.					
7) Claim(s) is/are objected to.					
8)⊠ Claim(s) <u>1-39</u> are subject to restriction and/or e	lection requirement.	• • •			
Application Papers					
9) The specification is objected to by the Examine	•.				
10) The drawing(s) filed on is/are: a) acce	epted or b) objected t	o by the Examiner.			
Applicant may not request that any objection to the		-			
Replacement drawing sheet(s) including the correcti	on is required if the drawir	ng(s) is objected to. See 37 C	FR 1.121(d).		
11) The oath or declaration is objected to by the Ex	•				
Priority under 35 U.S.C. § 119					
 12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 		. § 119(a)–(d) or (f).	e e ju		
2. Certified copies of the priority documents have been received in Application No					
3. Copies of the certified copies of the prior	ity documents have bee	en received in this National	Stage		
application from the International Bureau	(PCT Rule 17.2(a)).				
* See the attached detailed Office action for a list of	of the certified copies no	ot received.			
Address of the second of the s	•				
Attachment(s)	Д П	v Cumman (DTO 442)	•		
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) 		v Summary (PTO-413) o(s)/Mail Date			
3) Information Disclosure Statement(s) (PTO/SB/08)	5) 🔲 Notice o	f Informal Patent Application			
Paper No(s)/Mail Date	·				

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DETAILED ACTION

Election/Restrictions

1. Restriction to one of the following inventions is required under 35 U.S.C. 121:

I. Claims 1-17, drawn to method for logic operation to produce reference signals having three times said local frequency, for mixing with the frequency of input signals, to produce output at frequency at three times said local frequency less a center frequency of the input frequency, classified in class 455, subclass 318.

II. Claims 18-20, drawn to integrated circuit & the logic operation to produce three times local oscillator frequency, to produce output frequency at three times said local frequency less a center frequency of the input frequency, classified in class 455, subclass 318, 333.

III. Claims 21-39, drawn to integrated circuit of a mixer, having particular structure of 4 branches, each branch having three or more serially connected transistors, classified in class 455, subclass 323, 333, 550.1.

The inventions are distinct, each from the other because of the following reasons:

2. Inventions I, II and III are related as combination and subcombination. Inventions in this relationship are distinct if it can be shown that (1) the combination as claimed does not require the particulars of the subcombination as claimed for patentability, and (2) that the subcombination has utility by itself or in other combinations (MPEP § 806.05(c)). In the instant case, the combination as claimed does not require the particulars of the subcombination as claimed because

the subcombination in group I does not comprising the subcombination of the <u>integrated</u> <u>circuit</u>, the 4 branches of three or more transistors in Group II & Group III. Group I has distinct subcombination of <u>logic operation for generating three time said local oscillator</u> <u>frequency for producing output frequency at three times said local frequency less a center</u>

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frequency of the input frequency, which is different from Group II & III. Group II is the distinct subcombination of integrated circuit having operation logic for producing three times local oscillator frequency for producing output frequency at three times said local frequency less a center frequency of the input frequency, which is distinct from group III for the mixer having four branches having three transistors in each branch for the integrated circuit or device.

The subcombination has separate utility such as Group I for utility for a method of producing the output frequency at three times local frequency less a center frequency of the input frequency. Group II is for utility of integrated circuit for producing the output frequency at three times said local frequency less a center frequency of the input frequency. Group III is for utility of the integrated circuit of a mixer having 4 branches of three or more transistors

The examiner has required restriction between combination and subcombination inventions. Where applicant elects a subcombination, and claims thereto are subsequently found allowable, any claim(s) depending from or otherwise requiring all the limitations of the allowable subcombination will be examined for patentability in accordance with 37 CFR 1.104. See MPEP § 821.04(a). Applicant is advised that if any claim presented in a continuation or divisional application is anticipated by, or includes all the limitations of, a claim that is allowable in the present application, such claim may be subject to provisional statutory and/or nonstatutory double patenting rejections over the claims of the instant application.

3. Because these inventions are independent or distinct for the reasons given above and have acquired a separate status in the art in view of their different classification, restriction for examination purposes as indicated is proper.

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4. Applicant is advised that the reply to this requirement to be complete must include (i) an election of a species or invention to be examined even though the requirement be traversed (37 CFR 1.143) and (ii) identification of the claims encompassing the elected invention.

The election of an invention or species may be made with or without traverse. To reserve a right to petition, the election must be made with traverse. If the reply does not distinctly and specifically point out supposed errors in the restriction requirement, the election shall be treated as an election without traverse.

Should applicant traverse on the ground that the inventions or species are not patentably distinct, applicant should submit evidence or identify such evidence now of record showing the inventions or species to be obvious variants or clearly admit on the record that this is the case. In either instance, if the examiner finds one of the inventions unpatentable over the prior art, the evidence or admission may be used in a rejection under 35 U.S.C.103(a) of the other invention.

Conclusion

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Charles Chow whose telephone number is (571) 272-7889. The examiner can normally be reached on 8:00am-5:30pm. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Edward Urban can be reached on (571) 272-7899. The <u>fax</u> phone number for the organization where this application or proceeding is assigned is (571) 273-8300. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR

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only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic Business

Center (EBC) at 866-217-9197 (toll-free).

Charles Chow LC

February 6, 2007.

EDWARD F. URBAN SUPERVISORY PATENT EXAMINER

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